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| | APPLICANT DeWitt, Jr. et al. | |
| | FILING DATE | GROUP ART UNIT Not Assigned |

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EXAMINER Mark P. Francis

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| DATE CONSIDERED 8/9/07 | | EXAMINER Mark P. Francis | | | |
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| MPF | BU Not Assigned AUS920030478US1 | Levine et al. | Method and Apparatus for Selectively Counting Instructions and Data Accesses | Sep. 30, 2003 | |
| MPF | BV Not Assigned AUS920030479US1 | DeWitt, Jr. et al. | Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations | Sep. 30, 2003 | |
| MPF | BW Not Assigned AUS920030480US1 | DeWitt, Jr. et al. | Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold | Sep. 30, 2003 | |
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STATEMENT BY APPLICANT****Complete if Known**

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|----------------------|--------------------|
| Application Number | 10/757,171 |
| Filing Date | 01-14-2004 |
| First Named Inventor | DeWitt, Jr. et al. |
| Examiner Name | Not assigned |
| Art Unit | 2183 |
| Attorney Docket No. | AUS920030551US1 |

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| | Filing Date | | 2004-01-14 |
| | First Named Inventor | DeWitt, Jr. et al. | |
| | Art Unit | 2183 | |
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| Examiner Name | Unknown |
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| | Examiner Name | Unassigned | |
| | Attorney Docket Number | AUS920030551US1 | |

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| MPF | 4 | CONTE et al., "Using Branch Handling Hardware to Support Profile-Driven Optimization", Proceedings of the 27th Annual International Symposium on Microarchitecture, San Jose CA, 1994, pp. 12-21. | <input type="checkbox"/> |
| MPF | 5 | FISHER, "Trace Scheduling: A Technique for Global Microcode Compaction", IEEE Transactions on Computers, Vol. C30, No. 7, July 1981, pp. 478-490. | <input type="checkbox"/> |
| MPF | 6 | CHANG et al., "Using Profile Information to Assist Classic Code Optimizations", Software Pract. Exper. 21, December 1991, pp. 1301-1321. | <input type="checkbox"/> |
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| MPF | 8 | AHO et al., "Compilers: Principles, Techniques, and Tools", Addison-Wesley, 1988, pp. 488-497. | <input type="checkbox"/> |

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| | Art Unit | 2193 | |
| | Examiner Name | Unassigned | |
| | Attorney Docket Number | AUS920030551US1 | |

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|--|-----------------|---|--------------------------|
| MPF | 9 | INTEL, "Intel IA-64 Architecture Software Developer's Manual", Revision 1.1, Vol. 4, No. 245320.002, July 2001. | <input type="checkbox"/> |
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| | Filing Date | | 2004-01-14 |
| | First Named Inventor | DeWitt, Jr. et al. | |
| | Art Unit | 2193 | |
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